

FIG. 1

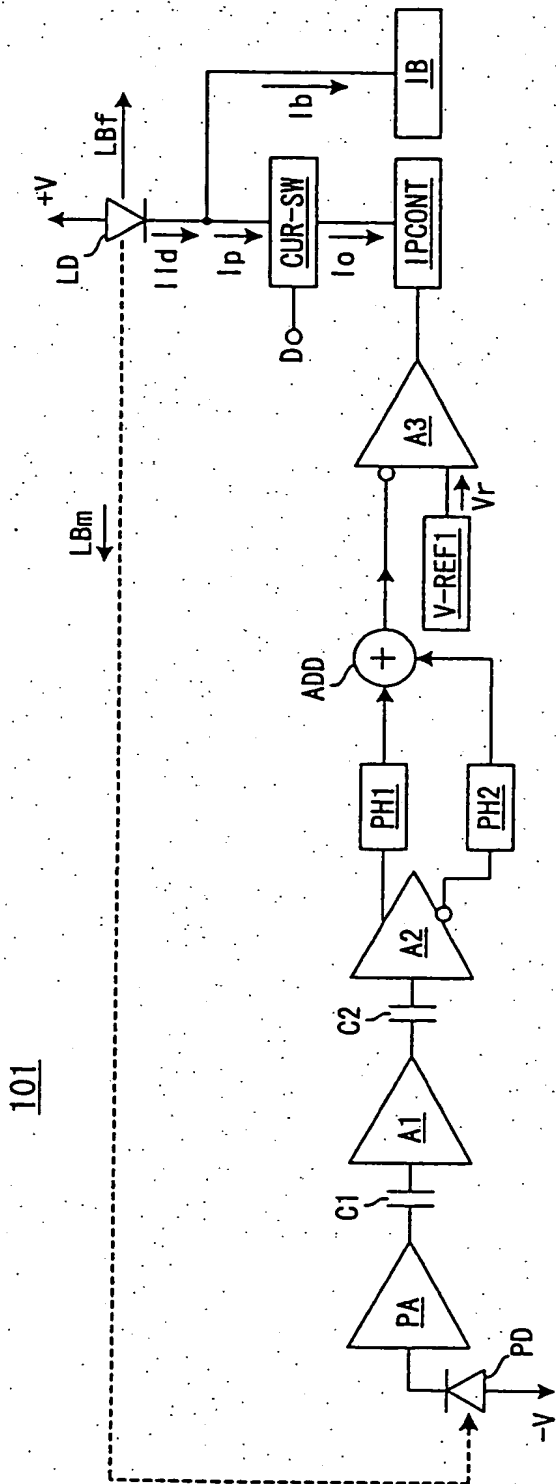


FIG. 2

FIG. 3

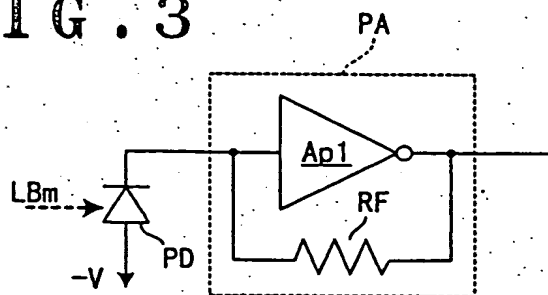


FIG. 4

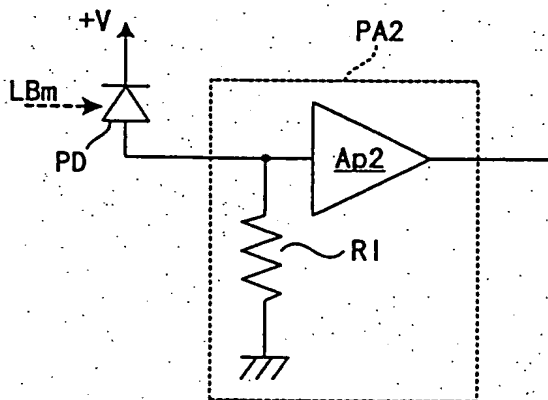


FIG. 5A

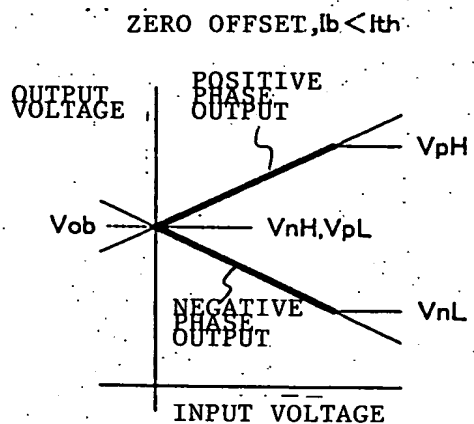


FIG. 5B

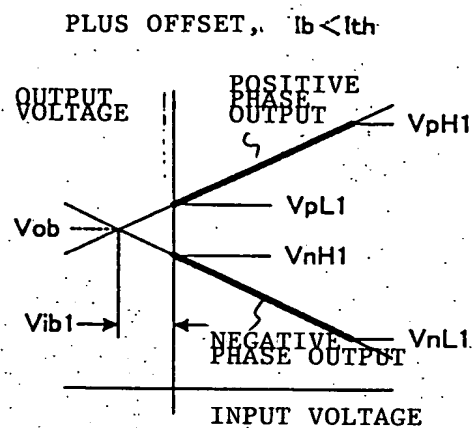


FIG. 5C

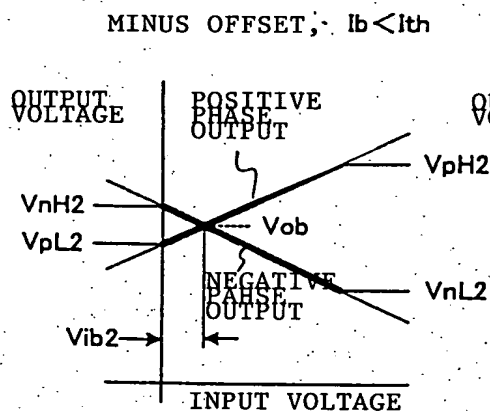
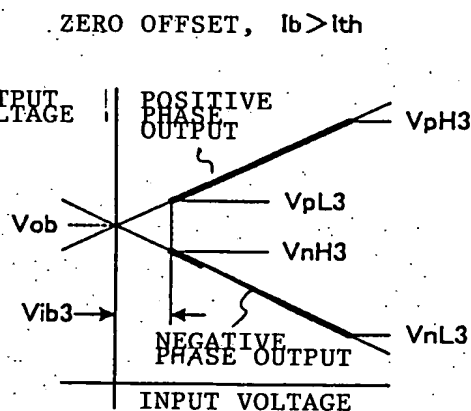
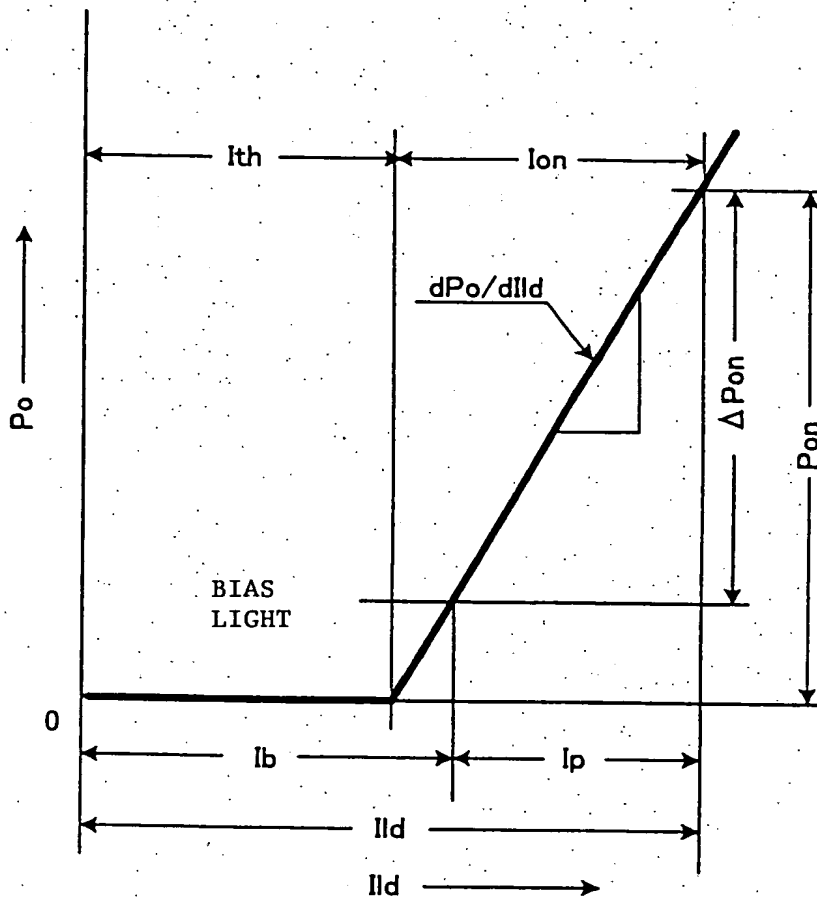


FIG. 5D

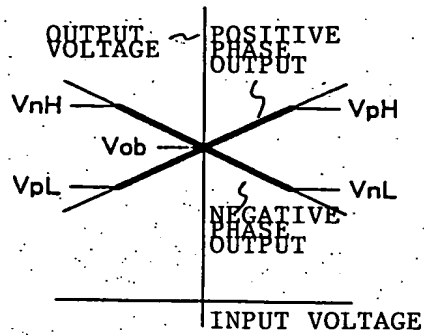




**FIG. 6**

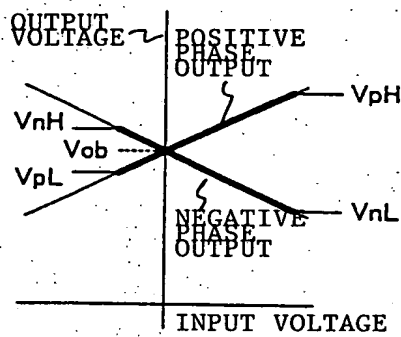
**FIG. 7A**

ZERO OFFSET  
 SAME "1/0" RATIO  
 AC COUPLED INPUT



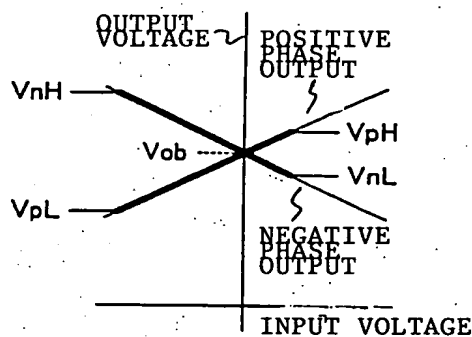
**FIG. 7B**

ZERO OFFSET  
 AC COUPLED INPUT



**FIG. 7C**

ZERO OFFSET  
 AC COUPLED INPUT



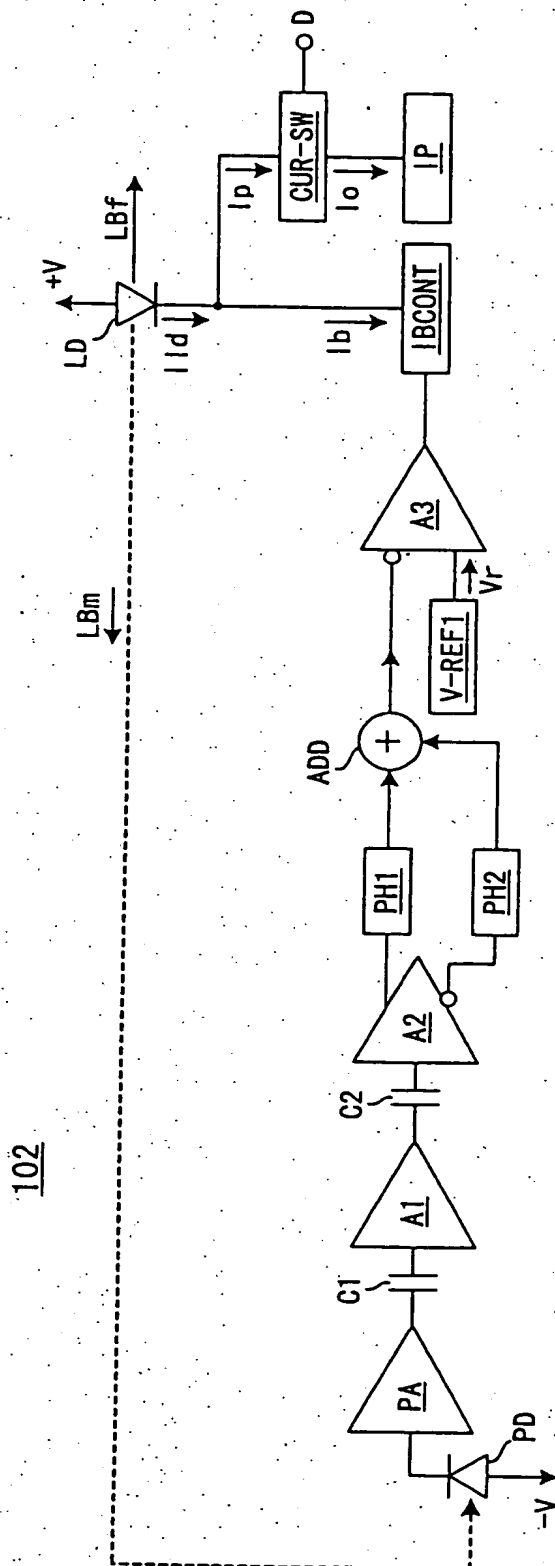
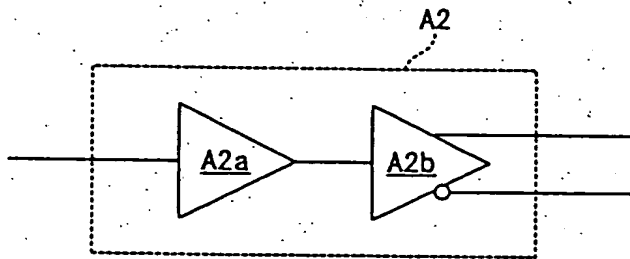


FIG. 8



**FIG. 9**





FIG. 10

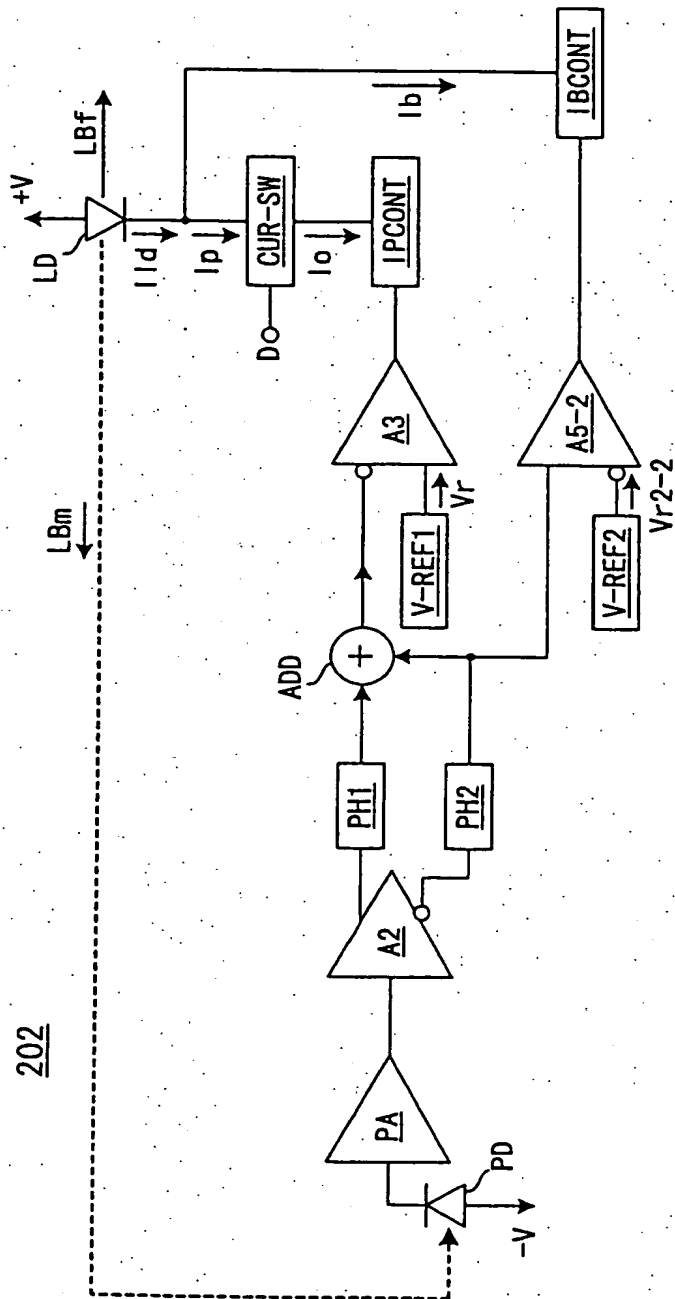


FIG. 11

FIG. 12A

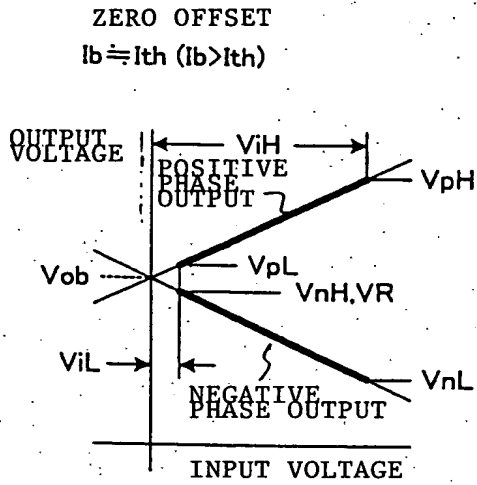


FIG. 12B

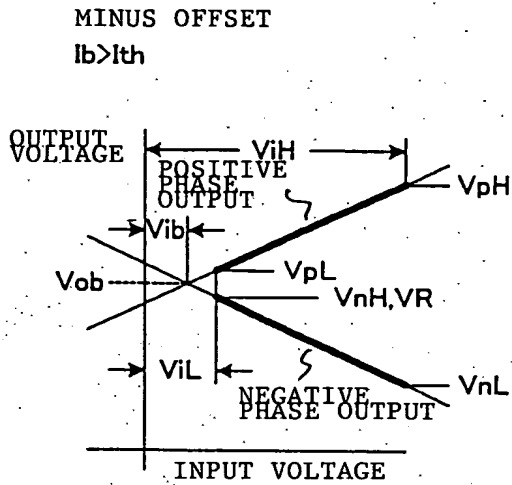


FIG. 12C

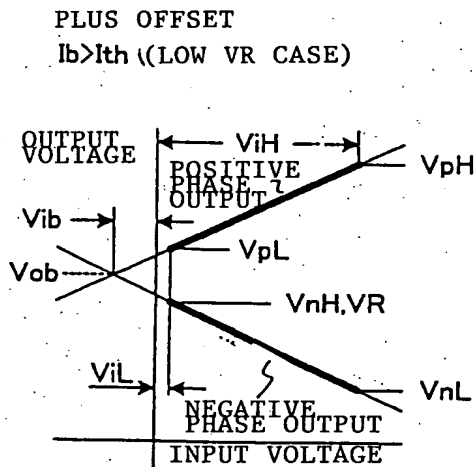
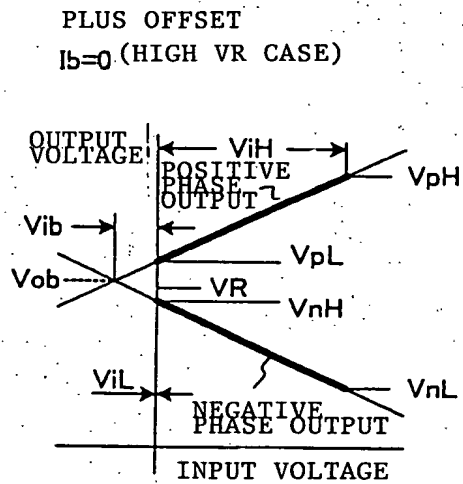


FIG. 12D



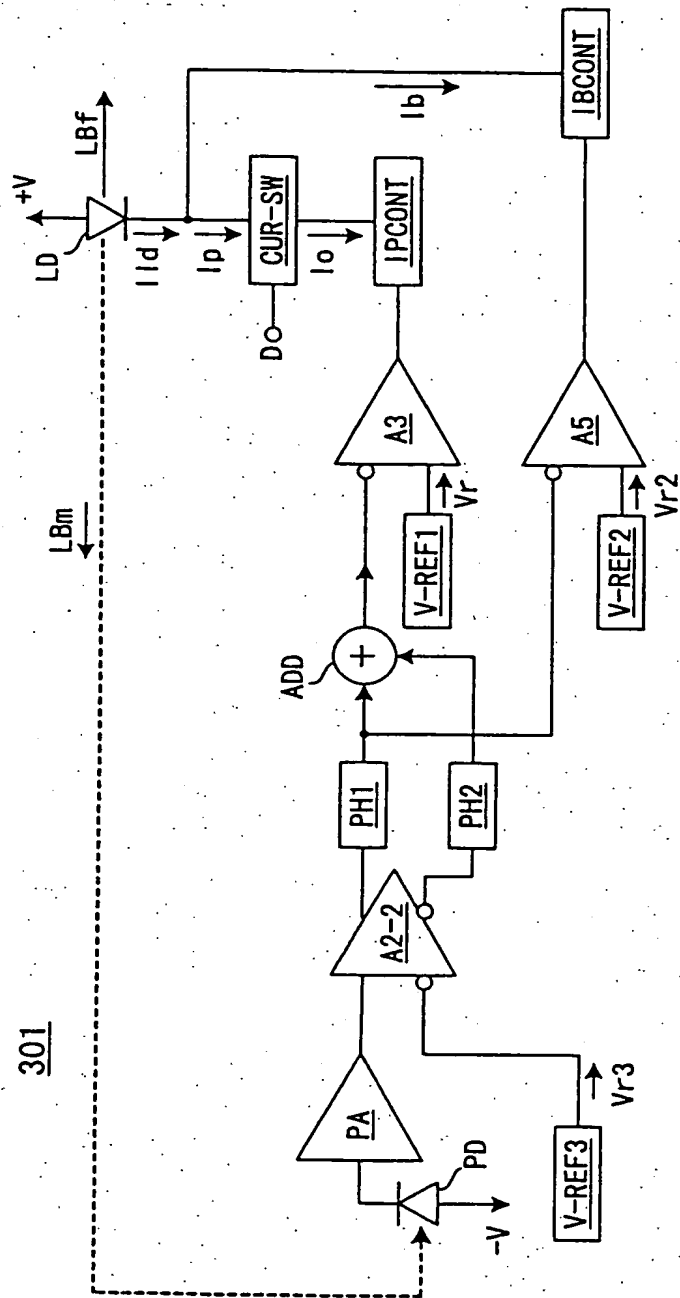


FIG. 13



FIG. 14

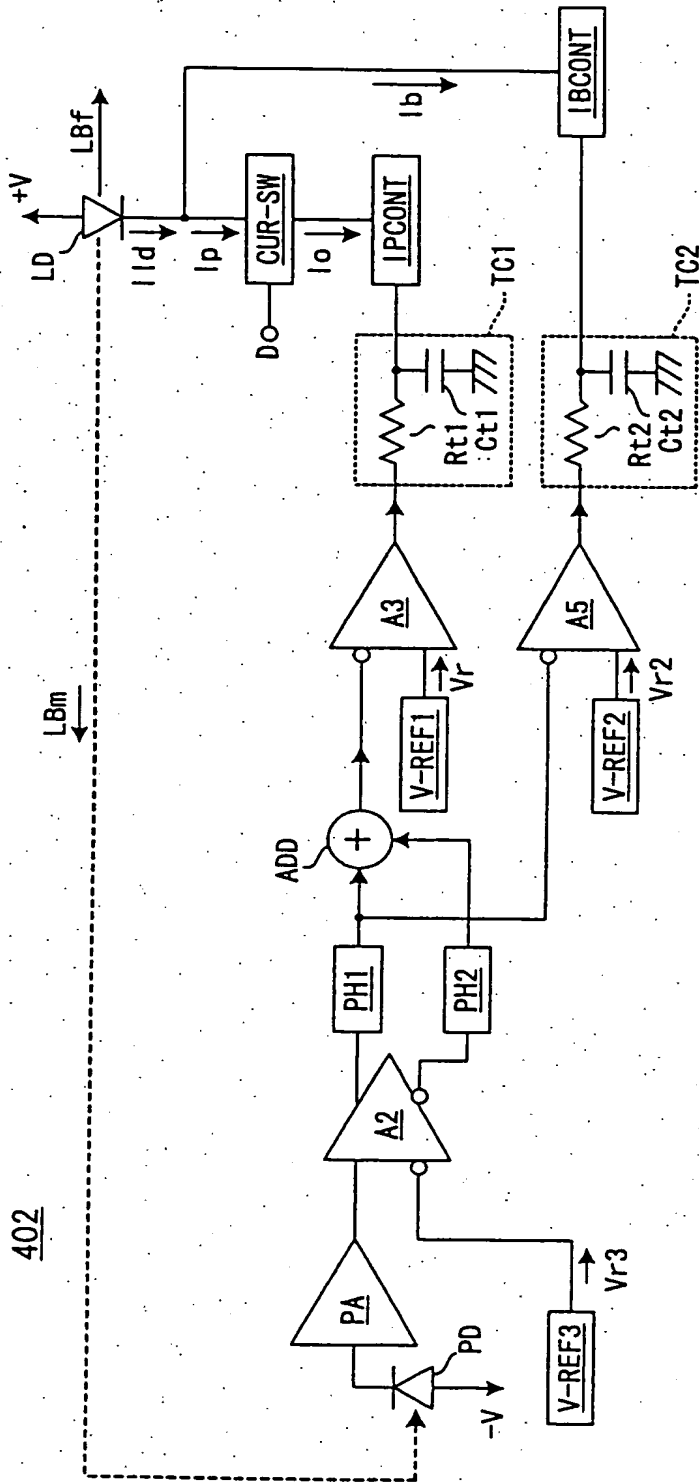


FIG. 15

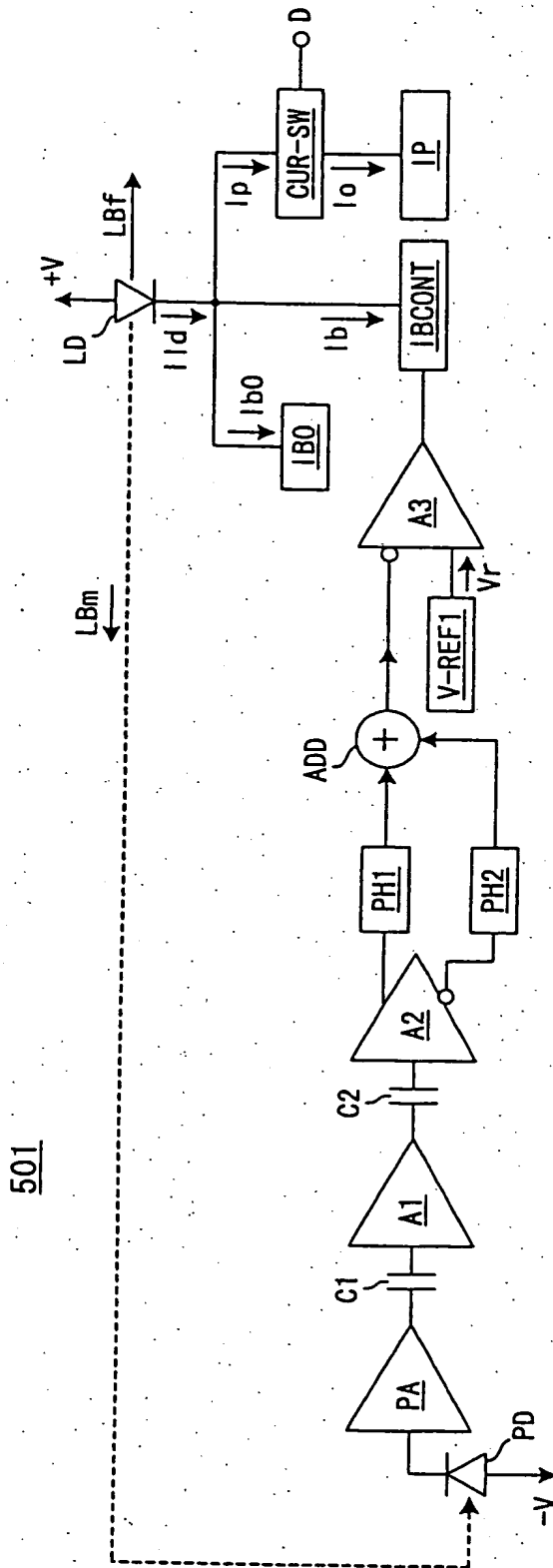


FIG. 16

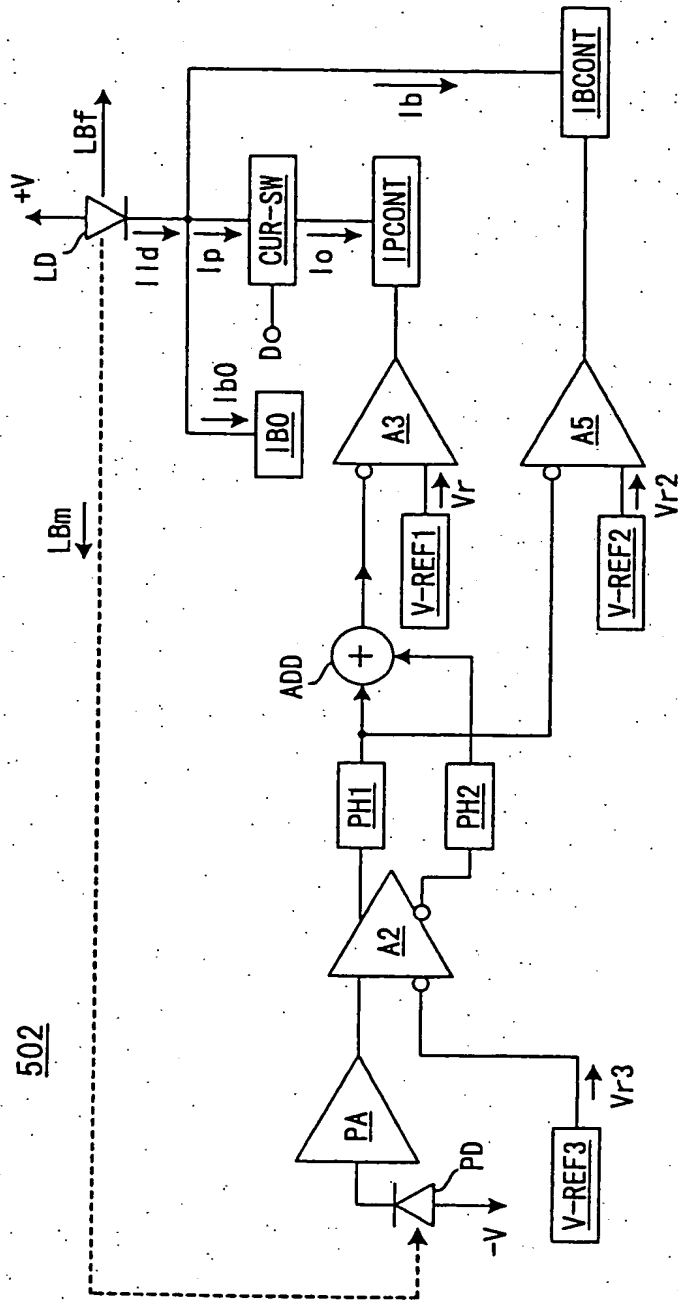


FIG. 17





**FIG. 18**



**FIG. 19**



**FIG. 20**

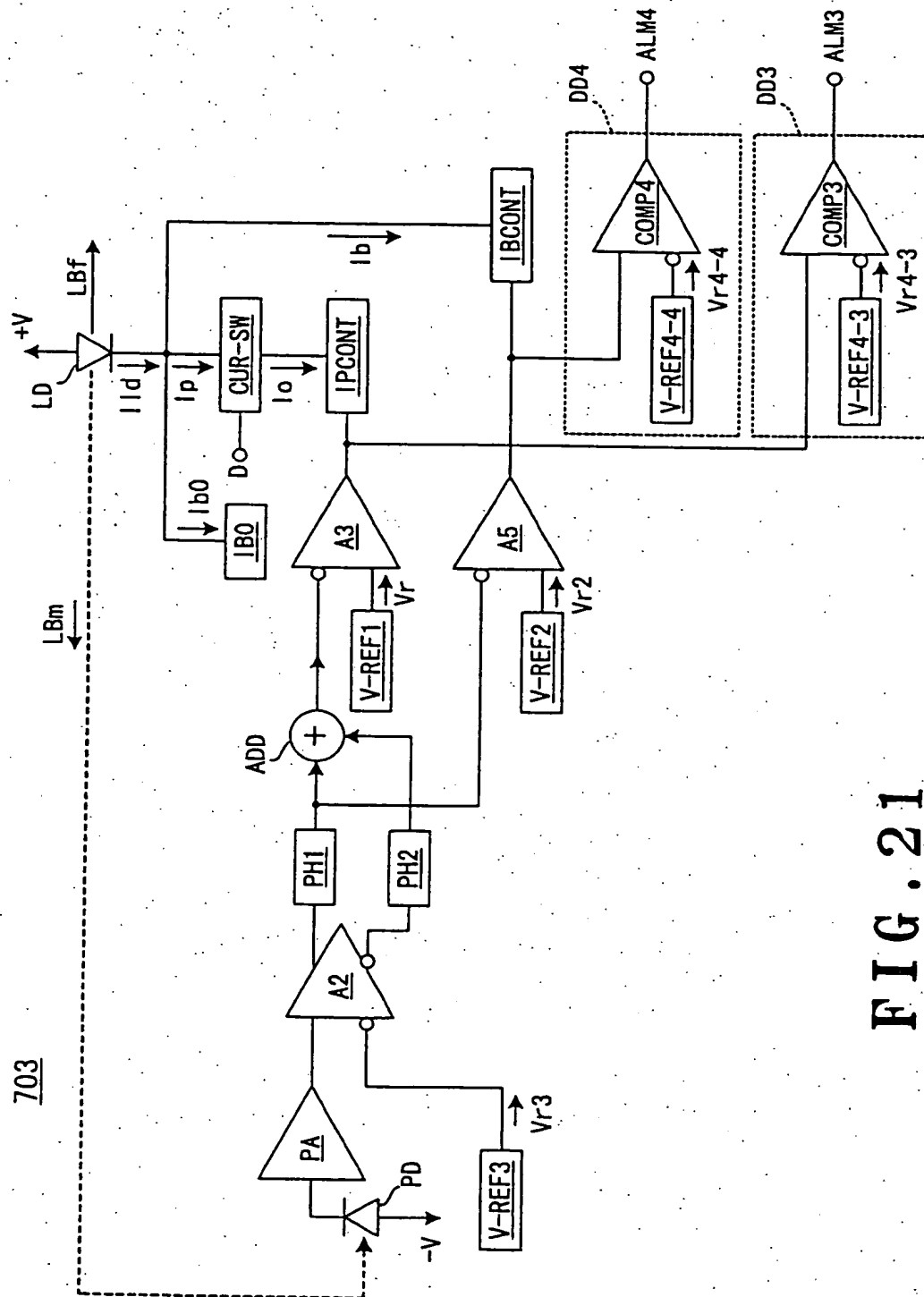


FIG. 21

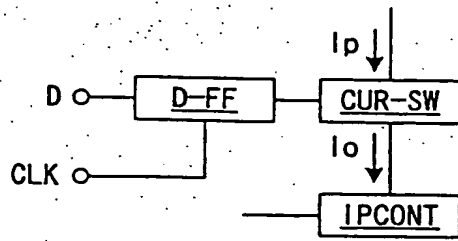


FIG. 22

FIG. 23A

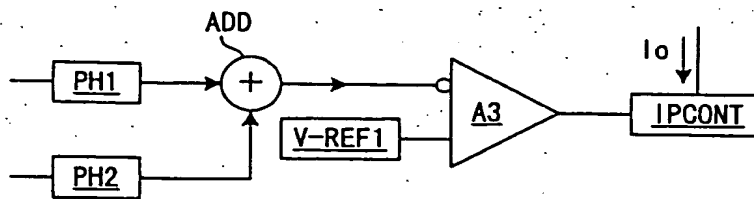


FIG. 23B

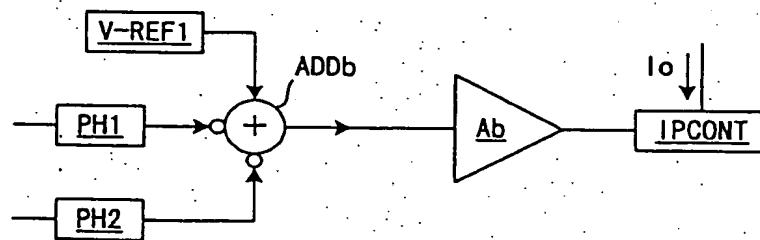


FIG. 23C

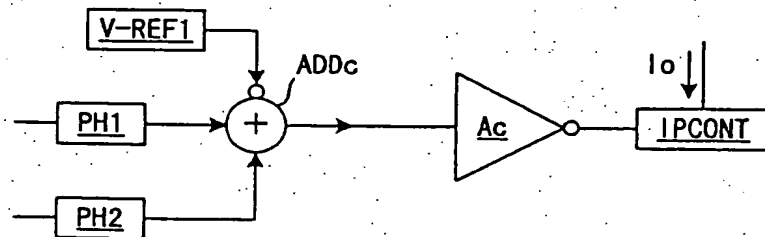


FIG. 23D

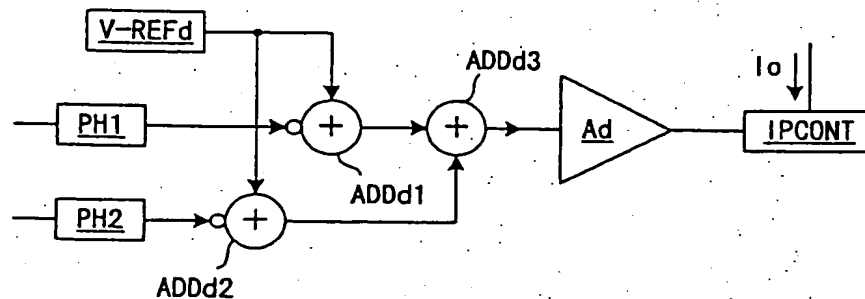


FIG. 24A

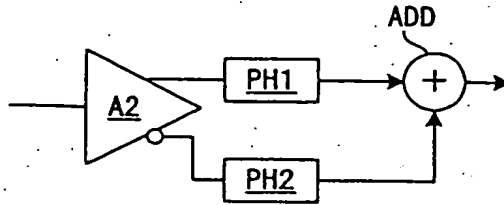


FIG. 24B

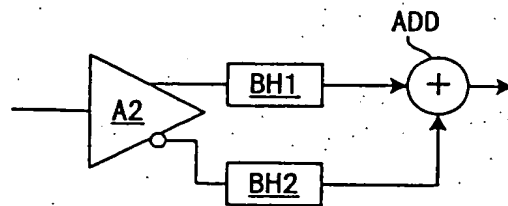


FIG. 24C

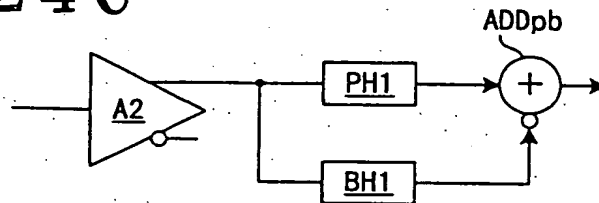


FIG. 25

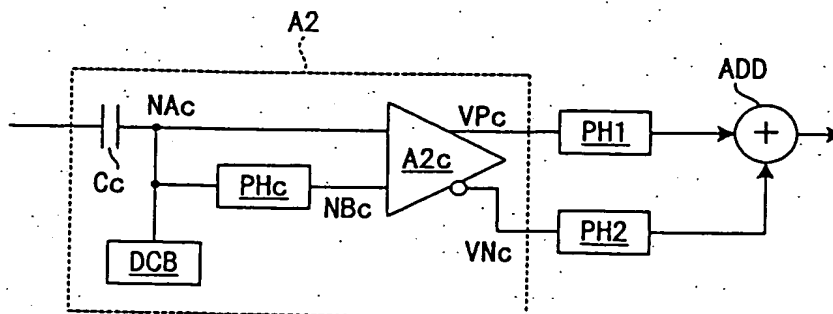


FIG. 26

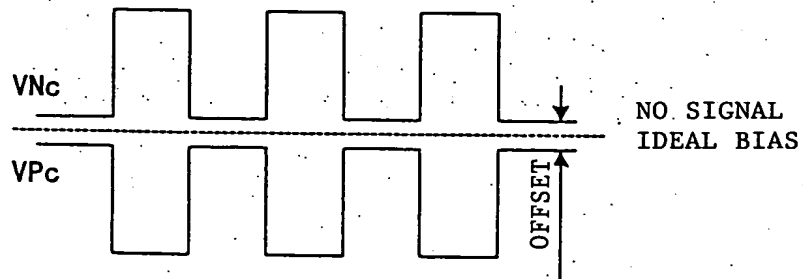


FIG. 27

